EE231
Electrical and Engineering Department
New Mexico Tech

First Class August 22, 2007

Instructor:
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Textbook:
Fundamentals of Digital Logic: with Verilog Design by Stephen Brown and Zvonko Vranesic.

Class Schedule
Mon,Wed, Fri 9:00 – 9:50 A.M., WORKC 113

Course Overview:
This course develops on a basic understanding of digital logic design. Nearly all systems that use some sort of signal processing, control, and communications equipment require digital design, this course is critical in the development of modern EE professionals. The text book offers a good introduction to students who will design and/or use digital logic design. We will cover much of the information discussed in chapters 2-9, and part of chapter 10. It is assumed that the students will read the introduction in chapter 1.

Course Structure:
Tentative Class Structure

Item	Description	Worth points
Homework	Homework will be assigned regularly and will be done on individual basis	10
Quizzes	Short quizzes will be given at any time and any student may be called upon to discuss any assignment.	10
Partial tests	Three tests, and each will contain material covered since previous test	3x20=60
Final test	The final test will cover all the material	20

Laboratory information:
Check http://www.ee.nmt.edu/~erives/231L_07/EE231L.html