

EE231
Fall, 2015

Instructor: Prof. Héctor Erives
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Office Hours: M-F 9:00-10:00

Course Description: This course develops on a basic understanding, design, and implementation of digital systems. Topics include number systems, Boolean logic, logic gates, design of combinatorial circuits, reduction of combinatorial circuits, register transfer logic, and design of complex digital systems. Synthesis, simulation and implementation of digital systems using Verilog.

Pre-requisites: EE 101, **Co-requisites:** EE 251 or CSE 113

Place in Curriculum: This course is open to majors and non-majors. The EE 231 is a pre-requisite to the EE 308 Microcontrollers course.

Course Learning Outcomes: The student should be familiar with basic digital design, and to design and analyze digital circuits in Verilog HDL.

Program Learning Outcomes: The ability to conduct experiments in digital electronics, and an ability to function on multidisciplinary teams.

Course Requirements:

Textbook: Fundamentals of Digital Logic with Verilog Design, Third Edition by Stephen Brown and Zvonko Vranesic

Late assignments are penalized 30%

Grading:

Homework assigned regularly: 20 %
Quizzes given regularly on Fridays: 20%
Two Partial exams (2x20%): 40%
Final comprehensive exam: 20%

Counseling and Disability Services:

Reasonable Accommodations

New Mexico Tech is committed to protecting the rights of individuals with disabilities. Qualified individuals who require reasonable accommodations are invited to make their needs known to the Office of Counseling and Disability Services (OCDS) as soon as possible. To schedule an appointment, please call 835-6619.

Counseling Services

New Mexico Tech offers mental health and substance abuse counseling through the Office of Counseling and Disability Services. The confidential services are provided free of charge by licensed professionals. To schedule an appointment, please call 835-6619.

Academic Honesty: New Mexico Tech's Academic Honesty Policy for undergraduate students is found starting on page 60 of the NMT Undergraduate Catalog,

http://www.nmt.edu/images/stories/registrar/2014-2015_UNDERGRADUATE_Catalog_FINAL.pdf

New Mexico Tech's Academic Honesty Policy for graduate students is found starting on page 59 of the NMT Graduate Catalog, http://www.nmt.edu/images/stories/registrar/2014-2015_GRADUATE_Catalog_FINAL.pdf.

You are responsible for knowing, understanding, and following this policy.

Respect Statement: New Mexico Tech supports freedom of expression within the parameters of a respectful learning environment. As stated in the New Mexico Tech Guide to Conduct and Citizenship: "New Mexico Tech's primary purpose is education, which includes teaching, research, discussion, learning, and service. An atmosphere of free and open inquiry is essential to the pursuit of education. Tech seeks to protect academic freedom and build on individual responsibility to create and maintain an academic atmosphere that is a purposeful, just, open, disciplined, and caring community."