

EE 554 Fall 2017

Instructor:

Prof. Hector Erives; Phone: 575-835-5932; Email: hector.erives@ee.nmt.edu

Textbook:

• Digital Control Engineering: Analysis and Design M. Sami Fadali and Antonio Visioli

Class Schedule

M,W, F 11:00 – 11:50, Workman 183

Office Hours

T,Tr: 9:00-10:00 A.M. or by appointment

Course Overview:

This course is intended for advanced engineering students interested in the application of micro-controllers and/or System-on-Chip (SOC) embedded control systems. The course will provide an overview of design of control methodologies using digital controllers. Projects will include the implementation of embedded real-time control solutions.

Item	Description	Points
Homework	Homework will be assigned regularly	20
Partial tests	Two mid-term exams.	2x20=40
Final project	Embedded control project	20
Final presentation	End-of-semester presentation about the project chosen	20

Tentative Class Structure

Academic honesty

All students are expected to demonstrate personal integrity. Interaction among students regarding homework assignments are strongly encouraged, however each student *must show his/her individual effort*. Exchange of information during in-class examinations as well as copying homework solutions from each other is strictly prohibited. Students exhibiting any form of academic dishonesty will be subject to penalties in accordance with NMT policies.