

EE 322 Advanced Analog Electronics, Spring 2008

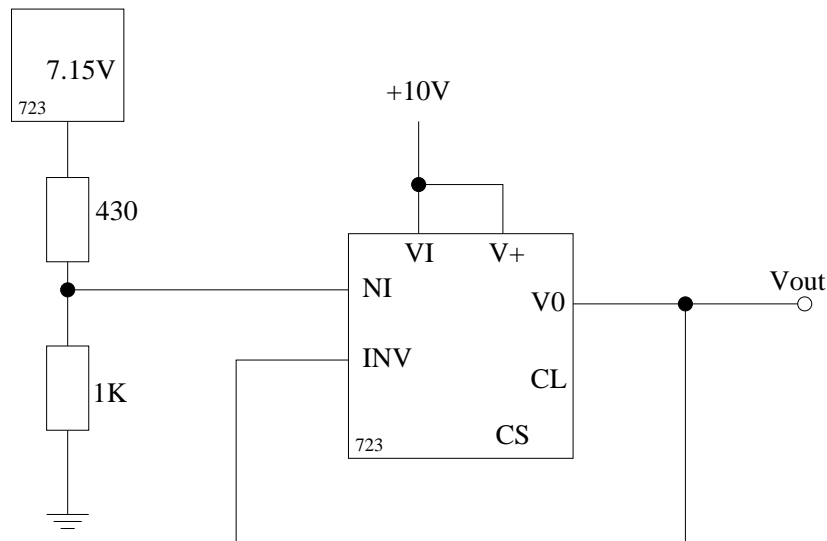
Test 1, February 22, 2008

Solutions

Linear regulator

If you mark your work clearly you are welcome to just redraw the changed portion of the circuit in each of the following questions.

1. Use a 723 to make a 5 V regulated supply from a 10 V unregulated input.

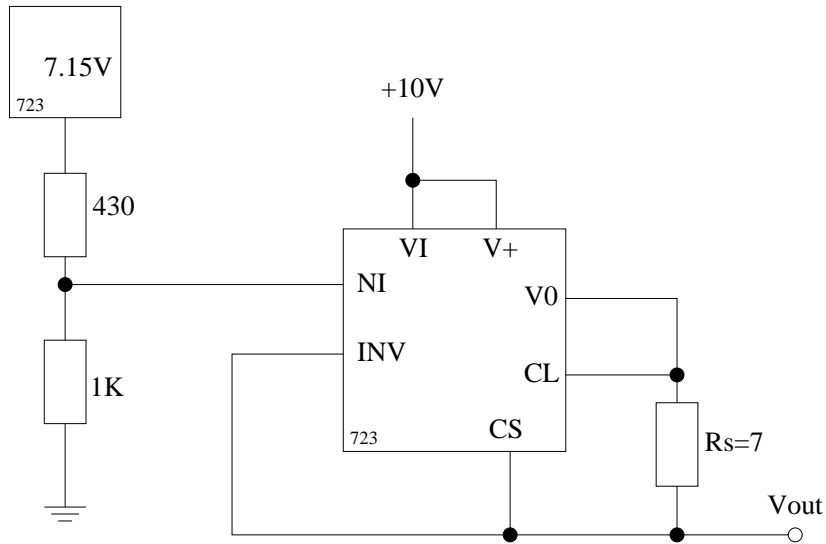


2. Add current limiting of 100 mA.

The current limiting resistor goes between CL and CS, and we take the output from CS. The size of the current limiting resistor is defined by

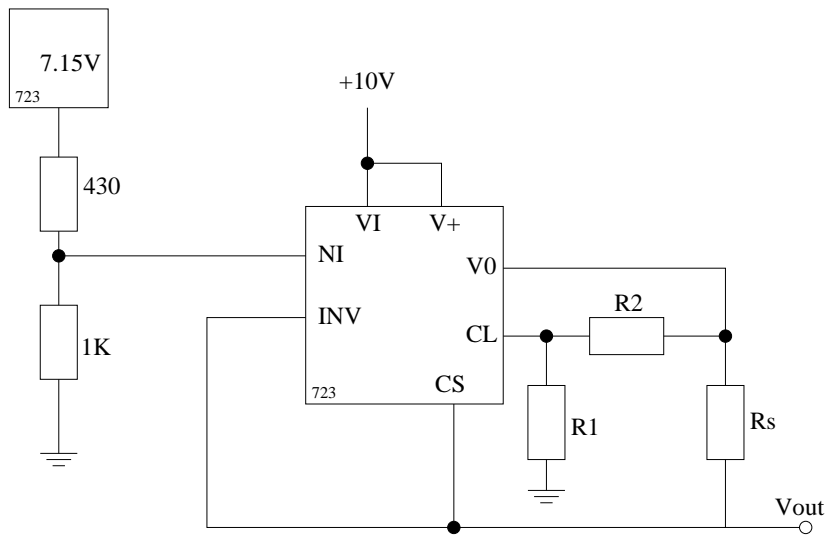
$$V_D = R_S I_{\max}$$

For $I_{\max} = 100 \text{ mA}$ and $V_D = 0.7 \text{ V}$, we get $R_S = 7 \Omega$. The modified circuit now looks like



3. **Modify to include foldback current limiting to 30 mA when the output is shorted.**

Now the circuit looks similar to Horowitz & Hill Figure 6.7, with a different values for the current sensing resistor, and two additional resistor R_1 and R_2 .



The equations we need are also listed in Figure 6.7,

$$\frac{I_{\max}}{I_{\text{SC}}} = 1 + \frac{R_2}{R_1 + R_2} \frac{V_{\text{reg}}}{V_{\text{BE}}}$$

We get

$$\frac{R_2}{R_1 + R_2} = \left(\frac{I_{\max}}{I_{\text{SC}}} - 1 \right) \frac{V_{\text{BE}}}{V_{\text{reg}}} = \left(\frac{100}{30} - 1 \right) \frac{0.7}{5} = 0.33$$

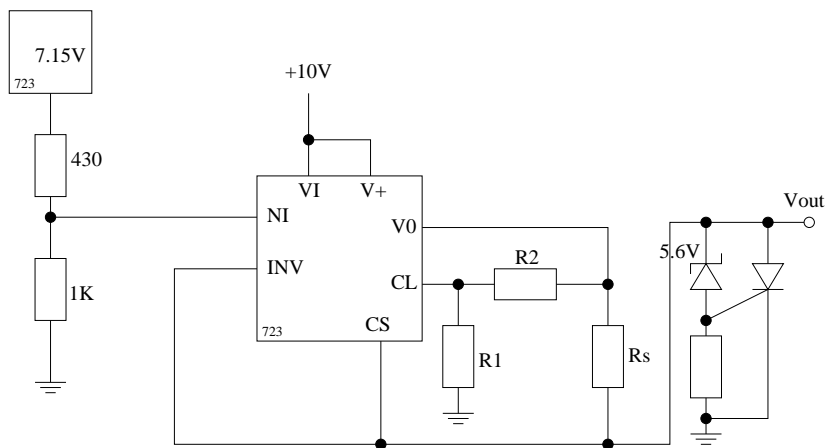
or $\frac{R_1}{R_2} = 2$. So let us choose $R_1 = 10\text{ k}\Omega$ and $R_2 = 5\text{ k}\Omega$. Next we want to find the size of the current-sensing resistor,

$$I_{SC} = \frac{1}{R_S} \frac{R_1 + R_2}{R_1} V_{BE}$$

or

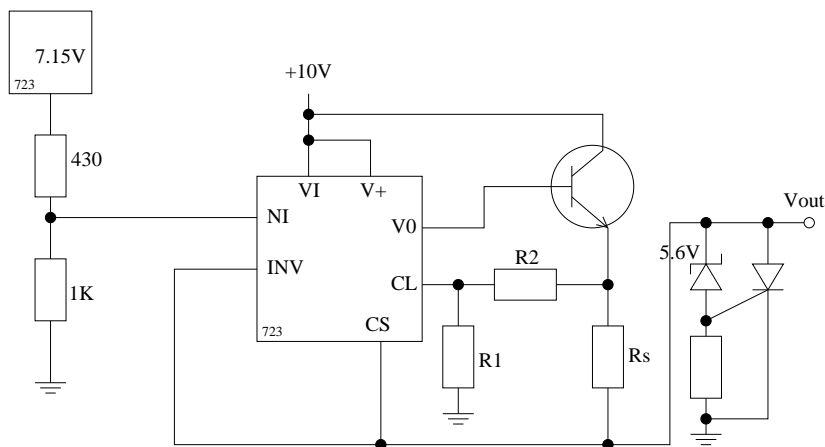
$$R_S = \frac{1}{I_{SC}} \frac{R_1 + R_2}{R_2} V_{BE} = \frac{1}{30 \times 10^{-3}} \frac{10 + 5}{5} 0.7 = 70\ \Omega$$

4. Add a overvoltage crowbar which activates at 6.2V. Use an SCR and a Zener diode.



The SCR turns on at roughly 0.6 V, so we use a 5.6 V Zener diode, and a small resistor (around 100 Ω).

5. Modify the circuit to use an external power transistor while raising the current limit to 1 A and foldback current to 300 mA.

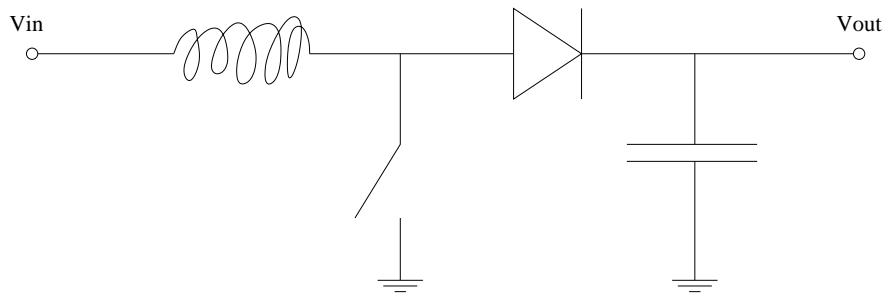


Since the ratio of maximum to short-circuit current is the same, we can keep the same values of $R_1 = 10\text{ k}\Omega$ and $R_2 = 5\text{ k}\Omega$. Since the short-circuit current is now ten times as large, the new value for the current-sensing resistor should be ten times as small, $R_2 = 7\Omega$.

Switching regulator

Label axis and voltages clearly. draw two full cycles, assume diode voltage drop is 0.7 V .

6. Draw a step-up switching regulator.

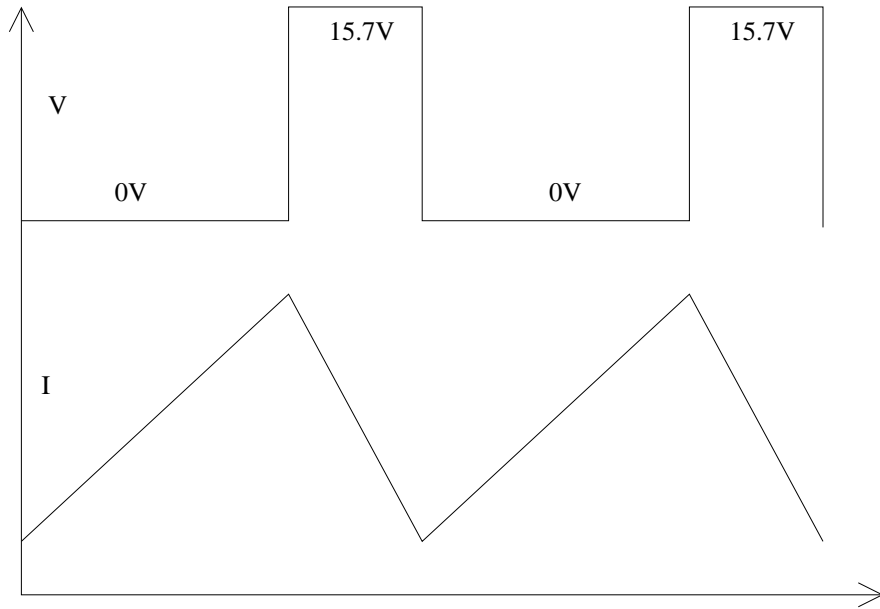


7. Draw the voltage at the inductor/diode node, and the inductor current, when input is 5 V , the output is 15 V , and the regulator is operating in continuous mode.

The important expression here is

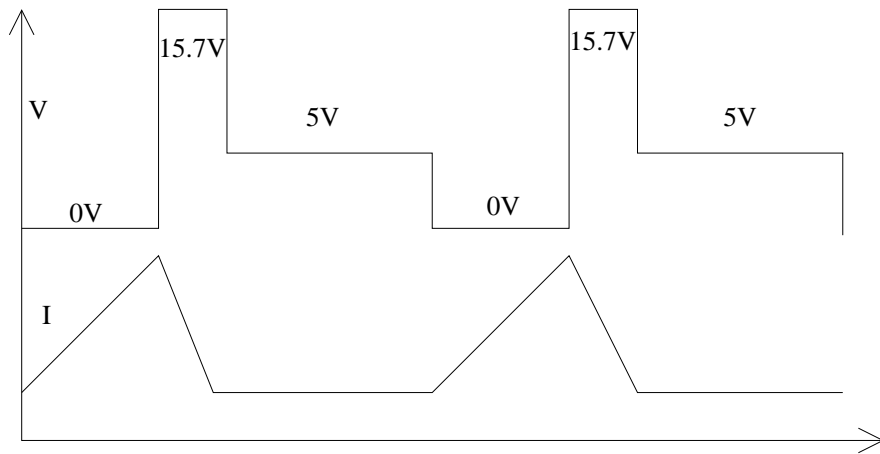
$$V = L \frac{dI}{dt}$$

When the switch is closed, the voltage across the inductor is 5 V . When the switch is open, the voltage across the inductor is -10 V . So the inductor discharges twice as fast as it charges. Therefore the charging cycle should be approximately twice as long as the discharging cycle.

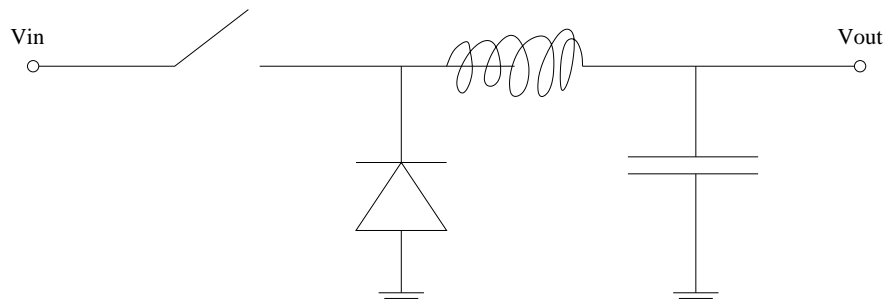


8. Same, but for discontinuous mode operation.

In discontinuous mode the inductor is completely discharged for part of the cycle when the switch is open. What that means is that first of all, the charge cycle is shorter than during discontinuous mode (because the slopes of the currents are still the same), and the voltage at the inductor/diode node is 5 V.



9. Draw a step-down switching regulator.

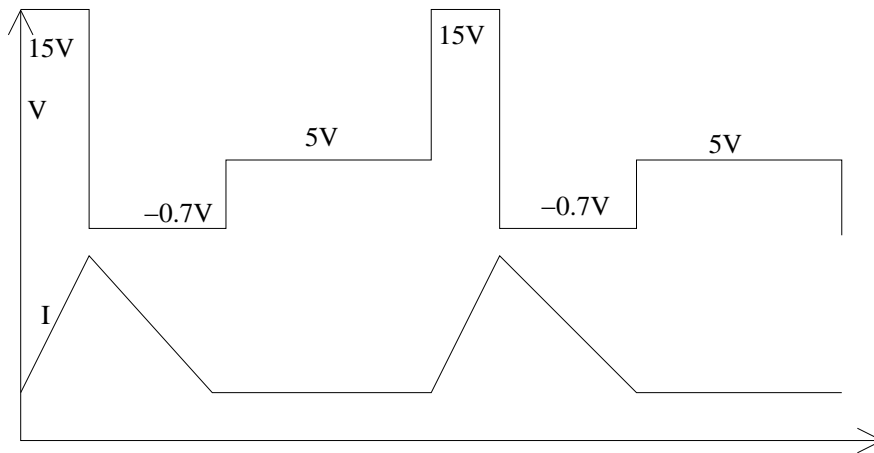


10. Draw the voltage at the inductor/diode node when the input voltage is 15 V and the output is 5 V, for discontinuous operation.

We again turn to the equation

$$V = L \frac{dI}{dt}$$

When the switch is closed, the voltage across the inductor is approximately 10 V. When the switch is open, the voltage across the inductor is approximately 5 V (ignoring the voltage drop across the diode). Thus the inductor charges twice as fast as it discharges. When the inductor is charging, the node is at the input voltage, 15 V. When the inductor is discharging, the node is at -0.7 V. When the inductor is completely discharge, the node is at the same voltage as the output, 5 V.



11. Specify a design that will allow the step-down to operate in continuous mode to a current of 10 mA.

We use the equation

$$I_{\min} = \frac{V_{\text{out}}}{2L} f^{-1} \left(1 + \frac{V_{\text{out}}}{V_{\text{in}}} \right)^{-2}$$

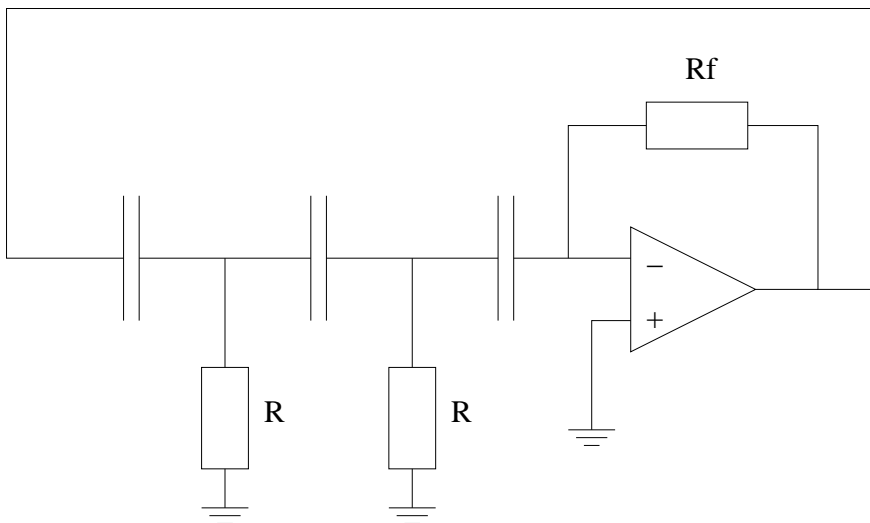
We pick values for L and f to make $I_{\min} = 10$ mA. I will arbitrarily pick $L = 10$ mH, and the figure out what f should be.

$$\begin{aligned} f &= \frac{V_{\text{out}}}{2L} \frac{1}{I_{\min}} \left(1 + \frac{V_{\text{out}}}{V_{\text{in}}} \right)^{-2} \\ &= \frac{5}{2 \times 10 \times 10^{-3}} \frac{1}{10 \times 10^{-3}} \left(1 + \frac{5}{15} \right)^{-2} \\ &= 14 \text{ kHz} \end{aligned}$$

Sinusoidal oscillator

Consider the phase-shift oscillator in Sedra & Smith Figure 13.8 (page 1175). The loop transfer function is given in exercise 13.5 at the bottom of the same page.

12. Draw the oscillator circuit without the output limiting circuitry.



13. Specify the component values which will set the oscillation frequency to 15 kHz

The loop transfer function is specified in the book, and is

$$L(j\omega) = \frac{\omega^2 C^2 R R_f}{4 + j \left(3\omega C R - \frac{1}{\omega C R} \right)}$$

We want the oscillation frequency $f_0 = 15 \text{ kHz}$, which means we want $\omega_0 = 2\pi 15 \times 10^3 \text{ s}^{-1}$. In order for that to be the oscillation frequency, the loop transfer function must have zero phase. That is the case when

$$3\omega_0 C R - \frac{1}{\omega_0 C R} = 0$$

or

$$C R = \frac{1}{\sqrt{3}\omega_0} = 6.1 \times 10^{-6}$$

If we choose $R = 1 \text{ k}\Omega$, then we get $C = 6.1 \times 10^{-6} / 10^3 = 6.1 \times 10^{-9} = 6.1 \text{ nF}$.

14. Specify any remaining component values such that oscillation will be sustained.

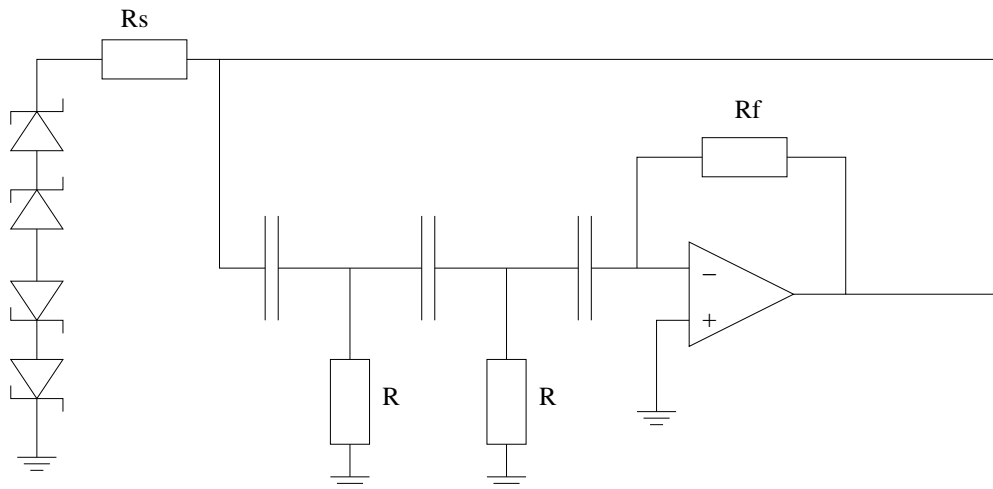
In order to sustain oscillations we also require the that amplitude of the loop gain at the oscillation frequency is at least 1. Thus

$$1 = |L(j\omega)| = \frac{1}{4}\omega_0^2 C^2 R R_f$$

or

$$R_f = \frac{4}{\omega_0^2 C^2 R} = \frac{4}{(2\pi 15 \times 10^3 \times 6.1 \times 10^{-9})^2 1 \times 10^3} = 12 \text{ k}\Omega$$

15. Design a approximately $\pm 12 \text{ V}$ limiting circuit for this oscillator using 5.6 V Zener diodes. Don't forget a small resistor. Where does it go, and why?



If we put two 5.6 V Zener diodes in series, the voltage drop across them will be 11.2 V . If we add to that the forward voltage drop across two other Zener diodes, the total voltage drop will be $11.2 + 1.4 = 12.6 \text{ V}$. So if we place four Zener diodes in series as shown on the figure, we have a $\pm 12.6 \text{ V}$ limiting circuit.

The small resistor is included in order to limit the amount of current that the op-amp must supply.