EE 322 Advanced Analog Electronics, Spring 2013 Handout 3: Amplifier stability - op-amp frequency response

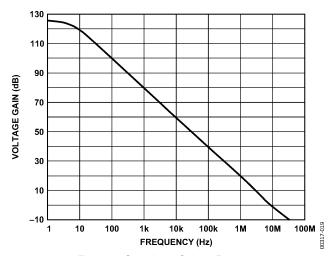


Figure 19. Open-Loop Gain vs. Frequency

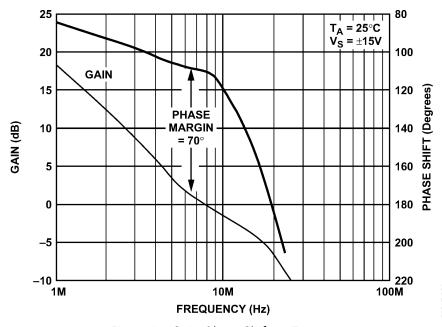


Figure 21. Gain, Phase Shift vs. Frequency

Parameter	Symbol	Conditions	OP27N Typical	Unit
AVERAGE INPUT OFFSET VOLTAGE DRIFT ¹	TCV _{os} or	Nulled or unnulled	0.2	μV/°C
	TCVosn	$R_P = 8 \text{ k}\Omega \text{ to } 20 \text{ k}\Omega$		
AVERAGE INPUT OFFSET CURRENT DRIFT	TClos		80	pA/°C
AVERAGE INPUT BIAS CURRENT DRIFT	TCI _B		100	pA/°C
INPUT NOISE VOLTAGE DENSITY	e _n	f ₀ = 10 Hz	3.5	nV/√Hz
	e _n	fo = 30 Hz	3.1	nV/√Hz
	e _n	f ₀ = 1000 Hz	3.0	nV/√Hz
INPUT NOISE CURRENT DENSITY	in	f ₀ = 10 Hz	1.7	pA/√Hz
	in	f _o = 30 Hz	1.0	pA/√Hz
	in	fo = 1000 Hz	0.4	pA/√Hz
INPUT NOISE VOLTAGE SLEW RATE	e _{np-p}	0.1 Hz to 10 Hz	0.08	μV p-p
	SR	$R_L \geq 2 \; k\Omega$	2.8	V/µs
GAIN BANDWIDTH PRODUCT	GBW		8	MHz

¹ Input offset voltage measurements are performed by automated test equipment approximately 0.5 seconds after application of power.