

EE 231L Lab 3**Prelab Part 1**

1. Read section 10.3.4 of the textbook and understand what is meant by switch debouncing.
2. Below is shown the values on S and R when the switch of Figure 10.48 is moved from the UP position to the Down position, then moved back to the UP position. Complete the timing diagram to show what will be on the Data (output of top NAND gate) and Data2 (output of bottom NAND gate) outputs.

