

EE 231 Prelab 5

Registers

In this lab we are going to investigate and build several sequential circuits. The behavior of sequential systems depends not only on the current values of the input variables, but also on the sequence of input values that occurred in the past. Such systems have some kind of storage of memory elements. In this lab we are going to design a couple types of registers.

1.Prelab

- 1.1.Design an eight-bit synchronous latch in Verilog.
- 1.2.Design an eight-bit PC register in Verilog.
- 1.3.Write a program which calls that above two designs to test that they function properly.