First Class January 18, 2017

Instructor:
Prof. Hector Erives; Phone: 505-835-5932; Email: erives@ee.nmt.edu

Texts:
- The HCS12/9S12: An Introduction to Software and Hardware Interfacing, by H.W. Huang (Not required)
- NXP Databooks on the MC9S12 (Available online)

Class Schedule
Mon, Wed, Fri 13:00 – 13:50

Office Hours
Tue- Thrs 9:00 - 10:00 A.M.

Course Overview:
This course develops on a basic understanding on the use of the MC9S12 microcontroller. The topics that we will address in this class include assembly and C language programming, peripherals of the MC9S12, time subsystems, A/D converter subsystem, serial peripheral interface (SPI) and serial communications.

Tentative Class Structure

<table>
<thead>
<tr>
<th>Item</th>
<th>Description</th>
<th>Worth points (%)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Homework</td>
<td>Homework will be assigned regularly</td>
<td>15</td>
</tr>
<tr>
<td>Quizzes</td>
<td>Short quizzes will be given regularly on Fridays</td>
<td>15</td>
</tr>
<tr>
<td>Partial tests</td>
<td>Three partial tests</td>
<td>2x15=30</td>
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<tr>
<td>Final test</td>
<td>Comprehensive final test</td>
<td>15</td>
</tr>
<tr>
<td>Laboratory</td>
<td></td>
<td>25</td>
</tr>
</tbody>
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Note: A 25% penalty applies to late work. You need to pass the Laboratory class to be able to pass the course.

Laboratory information:
Check http://www.ee.nmt.edu/~erives/classes.php web page for more information.

Academic honesty
All students are expected to demonstrate personal integrity. Interaction among students regarding homework assignments are strongly encouraged, however each student must show his/her individual effort. Exchange of information during in-class examinations as well as copying homework solutions from each other is strictly prohibited. Students exhibiting any form of academic dishonesty will be subject to penalties in accordance with NMT policies.