**EE 231, Digital Electronics, 4 cr, 3 cl hrs, 3 lab hrs**

*Corequisites: EE 251 or CSE 113 or ES 111*

*Normally offered fall semester*

Foundation of combinational digital system analysis and design; including Boolean algebra, logic gates, and truth tables. Sequential digital design via finite state machines.

Lab provides exposure to computer‐aided design software and programmable logic hardware.

 [NMT\_Course\_Catalog](https://www.nmt.edu/registrar/2017-2018%20Course%20Catalog%20FINAL.pdf) (page 282)

EE Curriculum at:

<http://ee.nmt.edu/docs/flowchart/nmt_ee_curriculum.html>