EE 231L Fall 2006

## EE 231L Lab 2

## Prelab Part 2

Design a 2-bit up-down counter using 3 different methods:

- 1. Use discrete gates and D flip-flops.
- 2. Write an AHDL text design file which uses a state transition table.
- 3. Write and AHDL text design file which uses D flip-flops and If-Then statements.