

EE 308 – Homework 9

Due Mar. 22, 2002

For all problems below assume you are using a 68HC912B32 chip with a 16 MHz crystal (which results in an 8 MHz timer clock).

1. Problem 1 (Fundamental) on Page 390 of the Text.
2. Problem 4 (Fundamental) on Page 390 of the Text.
3. What register is the Sequence Complete Flag (SCF) in? How does the SCF flag get set? How do you clear it?
4. Write some code which will enable the A/D converter, put it into 8-bit mode, and convert the analog inputs on pins PAD0 through PAD7 continuously.
5. Write some code which will enable the A/D converter, put it into 10-bit mode, and convert the analog inputs on pins PAD0 through PAD7 once. Add some code which will wait until the eight conversions are completed.
6. Write some code which will enable the A/D converter, put it into 8-bit mode, and convert the analog input on pin PAD3 eight times, then stop. Add some code which will wait until the eight conversions are completed.
7. Add some code to the above problem which will average the eight values of the conversions of PAD3.
8. On an HC12, VRL is connected to 1 V, and VRH is connected to 3 V. The A/D converter is set up to do 10-bit conversions.
 - (a) What voltage step will cause the A/D converter to change value?
 - (b) If the input to the A/D converter is 2.3 V, what number will result from a conversion?
 - (c) If the result of a conversion is 0x17B, what was the input voltage to the A/D converter?