

FINAL PROJECT: INTERFACING AND MOTOR CONTROL

WEEK 1

**PORT EXPANSION FOR THE MC9S12
SERIAL COMMUNICATIONS USING THE 9S12 SPI AND A REALTIME
CLOCK**

Pre-Lab

1. Write a preliminary Verilog program to implement the input and output ports, as discussed in [Homework 11](http://www.ee.nmt.edu/~rison/ee308/hw/hw11.pdf) (<http://www.ee.nmt.edu/~rison/ee308/hw/hw11.pdf>).