

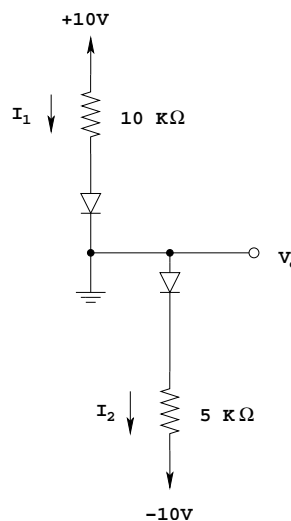
## EE 321 - Exam 2

October 14, 2002

Name: \_\_\_\_\_

Closed book. One page of notes and a calculator are allowed. Show all work. Partial credit will be given. No credit will be given if an answer appears with no supporting work.

1. In the circuit below assume the diodes are ideal. Find the labeled voltages and currents for the following circuit:



$V_O$  is connected directly to ground, so  $V_O = 0$  V.

Diode 1 is forward biased, so the voltage at the top of  $D_1$  is 0. There is 10 V across the  $10\text{ k}\Omega$  resistor, so  $I_1 = 1\text{ mA}$ .

Diode 2 is forward biased, so the voltage at the bottom of  $D_2$  is 0. There is 10 V across the  $5\text{ k}\Omega$  resistor, so  $I_2 = 2\text{ mA}$ .

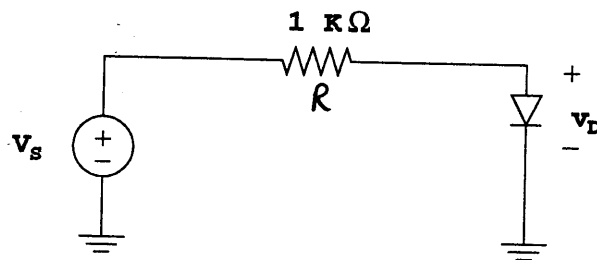
At the node with  $V_O$  there is 1 mA coming into the node from  $I_1$ , and there is 2 mA leaving the node from  $I_2$ . Since the total current must be zero, there must be another 1 mA coming into the node — this is coming from the ground connection.

We have  $v_{D_1} = 0$  V and  $i_{D_1} = 1$  mA, so this is okay.

We have  $v_{D_2} = 0$  V and  $i_{D_2} = 2$  mA, so this is okay.

- To show that  $D_1$  is not reverse-biased, assume that it is. Then  $I_1$  is 0, so there is no voltage drop across  $R_1$ . The voltage at the top of  $D_1$  is +10 V. We have  $V_{D_1} = 0$  V and  $V_{D_1} = +10$  V. A diode cannot have positive voltage across it, so  $D_1$  cannot be reverse biased.
- The same argument can be used to show  $D_2$  is not reverse biased.

2. The figure below shows  $v_D$  measured for two different source voltages  $V_S$ .



$V_S$	$v_D$	
2 V	0.526 V	1.474
10 V	0.595 V	9.405

- (a) Find the diode parameters  $n$  and  $I_S$ .  
 (b) Find the diode voltage  $v_D$  when  $V_S = 5$  V.

$$(a) \quad i_D = \frac{V_S - v_D}{R} \quad i_{D1} = \frac{2 - 0.526}{1k} = 1.474 \text{ mA} \quad i_{D2} = \frac{10 - 0.595}{1k} = 9.405 \text{ mA}$$

$$i_{D1} = I_S e^{\frac{v_{D1}}{nV_T}} \quad i_{D2} = I_S e^{\frac{v_{D2}}{nV_T}} \quad \text{Divide: } \frac{i_{D2}}{i_{D1}} = e^{\frac{v_{D2} - v_{D1}}{nV_T}}$$

$$\frac{v_{D2} - v_{D1}}{nV_T} = \ln\left(\frac{i_{D2}}{i_{D1}}\right) \quad n = \frac{v_{D2} - v_{D1}}{V_T \ln\left(\frac{i_{D2}}{i_{D1}}\right)} = \frac{0.595 - 0.526}{25 \text{ mV} \ln\left(\frac{9.405}{1.474}\right)} = 1.49$$

$$I_S = i_{D1} e^{-\frac{v_{D1}}{nV_T}} = 1.474 \text{ mA} e^{-\frac{0.526}{(1.49)(25 \text{ mV})}} = 1.09 \times 10^{-9} \text{ A} = 1.09 \text{ nA}$$

(b)  $v_D$  will be between 0.526 and 0.595. Start with  $v_D = 0.55$  V

$$i_D = \frac{V_S - v_D}{R} = \frac{5 - 0.55}{1k} = 4.45 \text{ mA}$$

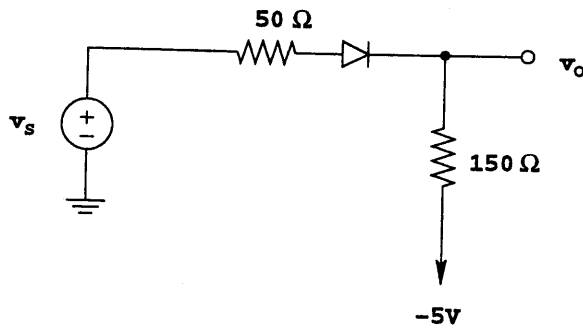
$$v_D = nV_T \ln\left(\frac{i_D}{I_S}\right) = (1.49)(25 \text{ mV}) \ln\left(\frac{4.45 \text{ mA}}{1.09 \text{ nA}}\right) = 0.567 \text{ V}$$

$$i_D = \frac{V_S - v_D}{R} = 4.43 \text{ mA}$$

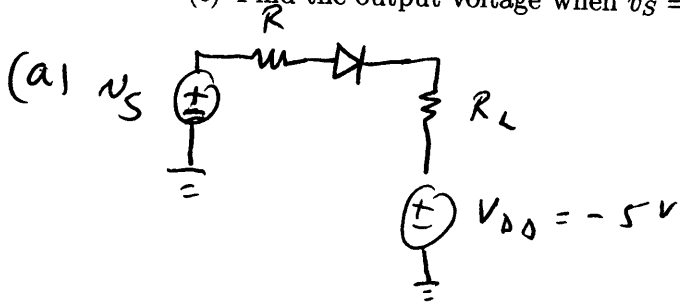
$$v_D = nV_T \ln\left(\frac{i_D}{I_S}\right) = (1.49)(25 \text{ mV}) \ln\left(\frac{4.43 \text{ mA}}{1.09 \text{ nA}}\right) = 0.567 \text{ V}$$

Same as before, so  $v_D = 0.567$  V

3. In the figure below assume  $n = 2$  for the diode. When  $v_s = 0$ , it is found that  $v_D = 0.6 \text{ V}$ .



- (a) Find the current through the diode  $I_D$  when  $v_s = 0$ .
- (b) Find the small-signal resistance of the diode  $r_d$ .
- (c) Find the output voltage when  $v_s = 1 \sin(\omega t)$ .



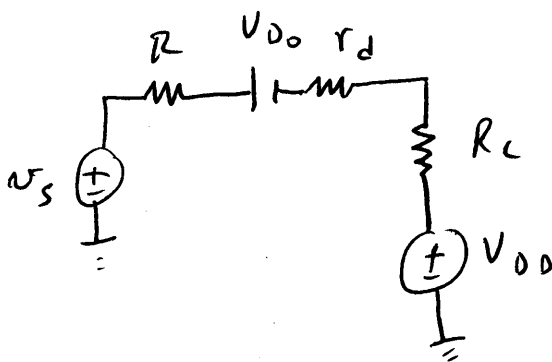
KVL:  $-v_s + i R + v_D + i R_L + V_{D0} = 0$

$$i = \frac{v_s - v_D - V_{D0}}{R + R_L}$$

$$= \frac{0 - 0.6 \text{ V} - (-5 \text{ V})}{50 \Omega + 150 \Omega} = \underline{\underline{22 \text{ mA}}}$$

(b)  $r_d = \frac{n V_T}{I_D} = \frac{2 (25 \text{ mV})}{22 \text{ mA}} = 2.27 \Omega$

(c)  $V_{D0} = v_D - I_D r_d = 0.6 - (22 \text{ mA})(2.27 \Omega) = 0.55 \text{ V}$



KVL:  $-v_s + i R + V_{D0} + i r_d + i R_L + V_{D0} = 0$

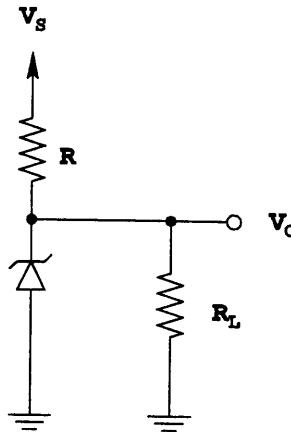
$$i_D = \frac{v_s - V_{D0} - V_{D0}}{R + r_d + R_L}$$

$$v_o = i_D R_L = \frac{v_s R_L - V_{D0} R_L}{R + r_d + R_L} + \frac{(-V_{D0} - V_{D0}) R_L}{R + r_d + R_L} = \frac{v_s R_L}{R + r_d + R_L} - V_{D0}$$

$$= 0.742 v_s - 1.7 \text{ V}$$

$$= 0.742 \sin(\omega t) - 1.7 \text{ V}$$

4. The figure below shows a zener regulator circuit. The zener diode has a zener voltage of  $V_Z = 10\text{ V}$  when the current through it is  $20\text{ mA}$ . It has a zener resistance  $r_Z = 20\ \Omega$ .

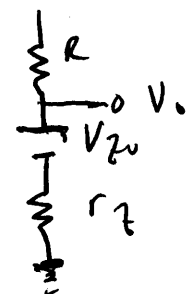


- Find the value of  $R$  such that the output voltage is  $10\text{ V}$  when  $V_S = 20\text{ V}$  and the load current is zero (i.e.,  $R_L = \infty$ ).
- Find the output voltage when  $V_S = 21\text{ V}$  and the load current is zero.
- Find the output voltage when  $V_S = 20\text{ V}$  and  $R_L = 1\text{ k}\Omega$ .
- What is the line regulation (percent change in output voltage for a change in  $V_S$ )?
- What is the load regulation (percent change in output voltage for a change in  $I_L$ )?

(a) ~~to have  $V_O = 10\text{ V}$  and  $V_S = 20\text{ V}$ , there is~~  
 To have  $V_O = 10\text{ V}$ ,  $I_Z$  must be  $20\text{ mA}$ . With  $I_L = 0$ ,  
 $I_R = I_Z$ , so  $I_R = 20\text{ mA}$   

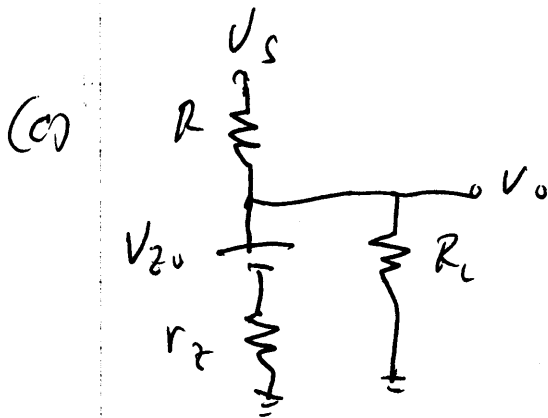
$$I_R = \frac{V_S - V_O}{R} \Rightarrow R = \frac{V_S - V_O}{I_R} = 500\ \Omega$$

(b) 
$$V_{Z0} = V_Z - I_{ZT} r_Z = 9.6\text{ V}$$



Using superposition

$$V_O = V_{Z0} \left( \frac{R}{R + r_Z} \right) + V_S \left( \frac{r_Z}{R + r_Z} \right) = 10.0385\text{ V}$$



Using superposition

$$V_o = V_{z0} \frac{R \parallel R_L}{R \parallel R_L + r_z} + V_s \frac{r_z \parallel R_L}{r_z \parallel R_L + R} = 9.81 \text{ V}$$

(d) A 1V change in  $V_s$  gave a 0.0385 V change in  $V_o$ , so

$$\frac{\Delta V_o}{\Delta V_s} = 38.5 \text{ mV/V} = 3.85\%$$

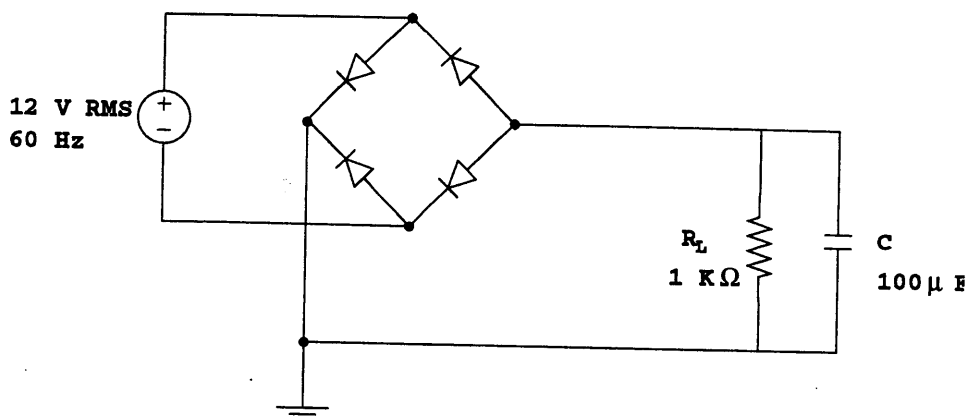
$$\text{Also, } \frac{\Delta V_o}{\Delta V_s} = \frac{r_z}{R + r_z} = 0.0385 = 3.85\%$$

(e)  $R_L = 1 \text{ k}\Omega = V_o = 9.81 \text{ V} \Rightarrow I_L = 9.81 \text{ mA}$

$$\frac{\Delta V_o}{\Delta I_L} = \frac{9.81 \text{ V} - 10 \text{ V}}{9.81 \text{ mA}} = -19.4 \text{ mV/mA}$$

$$\text{Also, } \frac{\Delta V_o}{\Delta I_L} = -(r_z \parallel R) = -19.23 \Omega = -19.23 \text{ mV/mA}$$

5. Consider the rectifier circuit in the figure below. Use the constant-voltage-drop diode model with  $V_D = 0.7$  V.



- (a) Sketch the output voltage waveform. Label the minimum and maximum voltages on the waveform.  
 (b) Find the voltage ripple  $V_r$ .  
 (c) Find the peak diode current  $i_{D_{max}}$ .

(a) See attached for plot. Note: The output voltage is negative

$$V_s = \sqrt{2} V_{RMS} = \sqrt{2} (12V) = 16.97V$$

$$V_p = V_s - 2V_D = 16.97V - 2 \cdot 0.7 = 15.57V$$

$$(b) V_r = \frac{V_p}{2fRC} = 1.41V$$

$$(c) i_{D_{max}} = I_L (1 + 2\pi \sqrt{V_p / 2V_r})$$

$$I_L = \frac{V_o}{R_L} \quad V_o = \frac{V_p - V_r/2}{R_L}$$

$$i_{D_{max}} = \left( \frac{V_p - V_r/2}{R_L} \right) (1 + 2\pi \sqrt{V_p / 2V_r}) = 238mA$$

