

EE 321
Fall 2002

Homework #14

Solutions

5.34

Exercise

V_{th} is the voltage where both the transistors are in saturation:

$$i_{DN} = \frac{1}{2} k_n' \left(\frac{W}{L}\right)_n (V_{th} - V_{tn})^2 \quad \text{Eqn 5.88}$$

$$i_{DP} = \frac{1}{2} k_p' \left(\frac{W}{L}\right)_p (V_{DD} - V_{th} - |V_{tp}|)^2 \quad \text{Eqn 5.90}$$

Because $i_{DP} = i_{DN}$ we have

$$\frac{1}{2} k_n' \left(\frac{W}{L}\right)_n (V_{th} - V_{tn})^2 = \frac{1}{2} k_p' \left(\frac{W}{L}\right)_p (V_{DD} - V_{th} - |V_{tp}|)^2$$

$$(V_{th} - V_{tn})^2 = r^2 (V_{DD} - V_{th} - |V_{tp}|)^2$$

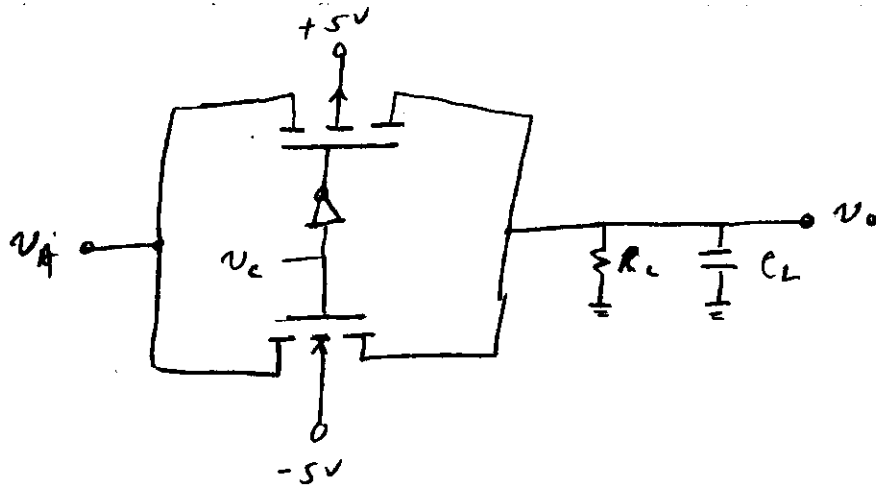
$$\text{where } r = \sqrt{\frac{k_p' \left(\frac{W}{L}\right)_p}{k_n' \left(\frac{W}{L}\right)_n}}$$

$$(V_{th} - V_{tn}) = r (V_{DD} - V_{th} - |V_{tp}|)$$

$$V_{th} + r V_{th} = r (V_{DD} - |V_{tp}|) + V_{th}$$

$$V_{th} = \frac{r (V_{DD} - |V_{tp}|) + V_{th}}{1 + r}$$

5.40
Exercise



$$|V_t| = 2V$$

$$k' \frac{W}{L} = 0.1 \text{ mA/V}^2$$

$$R_L = 50k$$

$$V_G = 5V \text{ (NMOS)}$$

$$V_G = -5V \text{ (PMOS)}$$

(a) For $v_A = -5V$

PMOS: $V_G = +5V$, $V_S = v_A = -5V$ $V_{GS} = 0V > V_{TP}$

PMOS off $r_{Dsp} = \infty$

NMOS: $V_G = 5V$ $V_S = v_A = -5V$ $V_{GS} = 5 - (-5) = 10V > V_{TN}$

NMOS on

$$r_{DSN} = \left[k' \frac{W}{L} (V_{GS} - V_{TN}) \right]^{-1} = \left[(0.1 \text{ mA/V}^2) (10V - 2V) \right]^{-1}$$

$$= 1.25 \text{ k}\Omega$$

$$R = r_{DSN} = 1.25 \text{ k}\Omega$$

$$v_o = \frac{R_L}{R + R_L} v_A = \frac{50k}{1.25k + 50k} (-5V) = -4.878V$$

(b) For $v_A = -2V$

PMOS: $V_S = v_A = -2V$ $V_{GS} = -3V < V_{TP}$

PMOS on

NMOS: $V_S = v_A = -2V$, $V_{GS} = 5 - (-2) = 7V > V_{TN}$

NMOS on

$$r_{Dsp} = \left[(0.1 \text{ mA/V}^2) | -3V - (-2V) | \right]^{-1} = 10 \text{ k}\Omega$$

$$r_{DSN} = \left[(0.1 \text{ mA/V}^2) | 7V - 2V | \right]^{-1} = 2 \text{ k}\Omega$$

$$R = r_{DSN} \parallel r_{DSP} = 2k\Omega \parallel 10k\Omega = 1.67k\Omega$$

$$v_o = \frac{R_L}{R + R_L} v_A = \frac{50k}{1.67k + 50k} (-2V) = -1.94V$$

(c) $v_A = 0V$

Pmos: $v_s = v_A = 0V$ $v_{GS} = -5V - 0V = -5V < V_{TP}$

Pmos on

Nmos: $v_s = v_A = 0V$ $v_{GS} = 5V - 0V = 5V > V_{TN}$

Nmos on

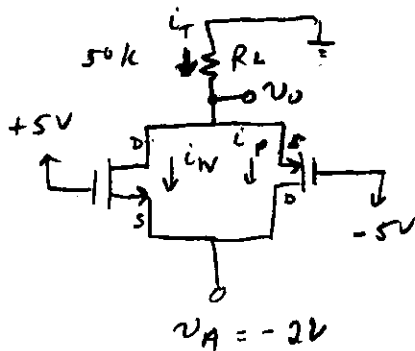
$$r_{DSP} = \left[(0.1 \text{ mA/V}^2) (-5V - (-2V)) \right]^{-1} = 3.33k\Omega$$

$$r_{DSN} = \left[(0.1 \text{ mA/V}^2) (5V - 2V) \right]^{-1} = 3.33k\Omega$$

$$R = r_{DSP} \parallel r_{DSN} = 3.33k \parallel 3.33k = 1.67k\Omega$$

$$v_o = \frac{R_L}{R + R_L} v_A = \frac{50k}{1.67k + 50k} 0V = 0V$$

Answer in the book for part (b) is slightly different. To get answer in book:



$$v_o = -i_T R_L = -(i_N + i_P) R_L \quad (1)$$

$$i_N = k_n' \left(\frac{W}{L} \right)_n \left[(v_{GSn} - V_{Tn}) v_{DSn} - \frac{1}{2} v_{DSn}^2 \right]$$

$$\approx k_n' \left(\frac{W}{L} \right)_n (v_{GSn} - V_{Tn}) v_{DSn}$$

$$i_N = 100 \mu\text{A/V}^2 (5 - (-2) - 2) (v_o - (-2)) \quad (2)$$

$$i_P \approx k_p' \left(\frac{W}{L} \right)_p (v_{GSp} - V_{Tp}) v_{DSp}$$

$$i_P = 100 \mu\text{A/V}^2 (-5 - v_o - (-2)) (-2 - v_o) \quad (3)$$

Put (2) + (3) into (1), and get an equation for v_o

$$v_o = -[(500 \mu\text{A/V})(v_o + 2) + (100 \mu\text{A/V}^2)(-3 - v_o)(-2 - v_o)] R_L$$

$$v_o = -25(v_o + 2) - 5(6 + 5v_o + v_o^2)$$

$$5v_o^2 + 58v_o + 80 = 0$$

$$v_o = -1.936\text{V}, -8.236\text{V}$$

-8.236 V doesn't make sense, so

$$v_o = -1.936\text{V}$$

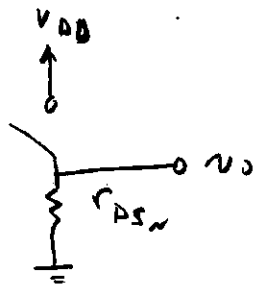
This gives $i_N = 31.9 \mu\text{A}$

$$i_P = 6.8 \mu\text{A}$$

$$i_T = 38.7 \mu\text{A}$$

$$R = \frac{v_o - v_A}{i_T} = \frac{-1.936\text{V} - (-2\text{V})}{38.7 \mu\text{A}} = 1.649\text{k}$$

5.92

(a) When $v_o = V_{OL}$, output of inverter looks like

$$R_o = r_{DS_n} = \left[k_n' \left(\frac{W}{L} \right)_n (V_{DD} - V_{th_n}) \right]^{-1}$$

$$= \left[(81 \mu\text{A}/\text{V}^2) \left(\frac{1.8 \mu\text{m}}{1.2 \mu\text{m}} \right) (5\text{V} - 0.75\text{V}) \right]^{-1}$$

$$= 1.94 \text{ k}\Omega$$

(b) When $v_o = V_{OH}$, output of inverter looks like

$$R_o = r_{DS_p} = \left[k_p' \left(\frac{W}{L} \right)_p (V_{DD} - |V_{th_p}|) \right]^{-1}$$

$$= \left[(27 \mu\text{A}/\text{V}^2) \left(\frac{5.4 \mu\text{m}}{1.2 \mu\text{m}} \right) (5\text{V} - 0.75\text{V}) \right]^{-1}$$

$$= 1.94 \text{ k}\Omega$$

5.93

Two ways:

(1) When output is low, voltage drop across r_{DS_n} is

$$0.2\text{V} = r_{DS_n} i_D \Rightarrow i_D = \frac{0.2\text{V}}{r_{DS_n}} = 0.10 \text{ mA}$$

When output is high, voltage drop across r_{DS_p} is

$$0.2\text{V} = r_{DS_p} i_D \Rightarrow i_D = \frac{0.2\text{V}}{1.94 \text{ k}\Omega} = 0.10 \text{ mA}$$

(2) When output low, nMOS is trioding; $v_{gs} = V_{DD}$

$$i_{Dn} = k_n' \left(\frac{W}{L} \right)_n \left[(V_{DD} - V_{th_n}) v_o - \frac{1}{2} v_o^2 \right]$$

$$= 81 \mu\text{A}/\text{V}^2 \left(\frac{1.8 \mu\text{m}}{1.2 \mu\text{m}} \right) \left[(5 - 0.75)(0.2\text{V}) - \frac{1}{2} (0.2\text{V})^2 \right]$$

$$= 0.11 \text{ mA}$$

Same for i_{Dp} when output high

5.100

Both transistors in saturation; $v_I = v_{EA} = 2.5V$

$$\begin{aligned}
 i_D = i_{DN} &= \frac{1}{2} k_n' \left(\frac{W}{L}\right)_n (v_I - V_{t_n})^2 \\
 &= \frac{1}{2} 81 \mu A/V^2 \left(\frac{1.8 \mu m}{1.2 \mu m}\right) (2.5V - 0.75V)^2 \\
 &= 0.18 \text{ mA}
 \end{aligned}$$

5.103

$$t_{PHL} = \frac{1.6C}{k_n' \left(\frac{W}{L}\right)_n V_{DD}} \quad \text{eqn 5.102}$$

$$\begin{aligned}
 W_n &= \frac{1.6C}{t_{PHL} k_n' \frac{1}{L_n} V_{DD}} \\
 &= \frac{1.6(0.05 \text{ pF})}{(60 \text{ ps})(81 \mu A/V^2) \left(\frac{1}{1.2 \mu m}\right) 5V} \\
 &= 6.3 \mu m
 \end{aligned}$$

$$\begin{aligned}
 W_p &= \frac{1.6C}{t_{PLH} k_p' \frac{1}{L_p} V_{DD}} \\
 &= 12 \mu m
 \end{aligned}$$

5.107

$$r_{DS} = \frac{1}{k_n' \left(\frac{W}{L}\right) (V_{GS} - V_t)}$$

When on, $V_G = V_C = +10V$

$$V_{GS} = V_G - V_S = V_G - V_{in} = 10V - V_{in}$$

For $V_{in} = +5V$:

$$r_{DS} = \frac{1}{(100 \mu A/V^2)(10 - 5 - 1)} = 2.5 k\Omega$$

For $V_{in} = -5V$:

$$r_{DS} = \frac{1}{(100 \mu A/V^2)(10 - (-5) - 1)} = 0.71 k\Omega$$

Want $\frac{r_{DS}}{R + r_{DS}} < 0.01$

$$R > \frac{r_{DS}}{0.01} - r_{DS} = 99 r_{DS}$$

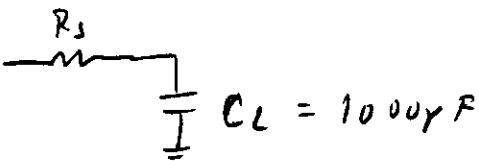
For $r_{DS} = 2.5 k\Omega$, need $R > 248 k\Omega$

5.109

Input is about 0V, so $r_{DSn} = r_{DSp}$, and $V_{GSn} = 5V - 0V = 5V$

$$R = r_{DSn} \parallel r_{DSp} = \frac{1}{2} r_{DSn} = \frac{1}{2} \frac{1}{k_n' \left(\frac{W}{L}\right)_n (V_{GS} - V_{tn})}$$

$$= \frac{1}{2} \frac{1}{(100 \mu A/V^2)(5V - 1V)} = 1.25 k\Omega$$

When switch is on, v_i  $C_L = 1000 \mu F$

$$f_0 = \frac{1}{2\pi R_s C_L} = 127 \text{ kHz}$$