Circuit Analysis Principles 103 182 Circuits

2.26 Assume clockwise mesh currents for the circuit shown in Fig. P2.26 (below). Use mesh analysis to find these mesh currents.

2.27 For the circuit shown in Fig. P2.27, find ve when the ideal amplifier (a) is an op amp, and (b) has finite gain A.



Fig. P2.27

2.28 For the op-amp circuit shown in Fig. P2.28, find (a) v_o , and (b) i_o .



2.29 For the op-amp circuit shown in Fig. P2.29,

find (a) v_o , and (b) i_o .

 $\leq R_1$ Fig. P2.29

2.30 The op-amp circuit shown in Fig. P2.30 is known as a negative-impedance converter. For this circuit, find (a) v_o , and (b) the resistance v_s/i_s .



find (a) v_o , and (b) the resistance v_s/i_s . (See p. 104.)

 v_o , and (b) the resistance v_s/i_s . (See p. 104.)

AC Analysis 257

3.40 For the parallel *RC* circuit given in Fig. P3.8, suppose that $i_s(t) = 6u(t)$ A. Find the step responses v(t) and i(t), and sketch these functions. 3.41 For the parallel RL circuit given in Fig. P3.17, find the unit step responses $i_L(t)$ and v(t), and

sketch these functions. **3.42** For the circuit shown in Fig. P3.42, find the step responses v(t) and i(t), and sketch these func



Fig. P3.42

3.43 For the circuit given in Fig. P3.30, suppose that $i_s(t) = 10u(t)$ A. Use Thévenin's theorem to find the step responses i(t) and v(t), and sketch these func-

3.44 For the circuit given in Fig. P3.30, replace the inductor with a 0.1-F capacitor. Suppose that $i_s(t) =$ 10u(t) A. Use Thévenin's theorem to find the step responses v(t) and i(t), and sketch these functions.

3.45 For the circuit given in Fig. P3.34, suppose that $v_s(t) = 12u(t)$ V. Find the step responses v(t) and 2.31 For the op-amp circuit shown in Fig. P2.31, *i*(*t*), and sketch these functions.

3.46 For the circuit given in Fig. P3.34, replace 2.32 For the op-amp circuit shown in Fig. P2.31, the capacitor with a 3-H inductor. Suppose that interchange the 1- Ω and 2- Ω resistors, and find (a) $(i_s) = 12u(t)$ V. Find the step responses i(t) and v(t), and sketch these functions.

3.47 The step responses $v_C(t)$ and i(t) for the series RC circuit shown in Fig. P3.47a are given by Eq. 3.19 and Eq. 3.20, respectively. Use duality to deter mine the step responses $i_L(t)$ and v(t) for the parallel GL circuit shown in Fig. P3.47b.

3.48 Find the step response $v_o(t)$ for the op-amp circuit shown in Fig. P3.48.



Fig. P3.48

3.49 Find the step responses v(t) and $v_o(t)$ for the op-amp circuit shown in Fig. P3.49.





4.19 Find the frequency-domain Thévenin equivalent (to the left of terminals a and b) of the circuit m in Fig. 4.20 on p. 211. (Hint: Use the fact that

The frequency-domain Thévenin equivalent a circuit having $\omega = 5$ rad/s has $V_{oc} = -15.9^{\circ}$ V and $Z_{o} = 2.38 - j0.667$ fb. Deter-





mp circuit given in Fig. P4.22, = $3 \cos 2t V$.



Fig. P4.24

4.25 Use mesh analysis to find I_1 and I_2 for the circuit given in Fig. P4.25 when $V_{r1}=250\sqrt{2}/-30^\circ$ V, $V_{s2}=250\sqrt{2}/-90^\circ$ V, and $\mathbf{Z}=26-j15$ $\overline{\Omega}.$





V.

4.26 For the circuit shown in Fig. P4.9, when $i_s(t) = 5 \cos 3t$ A then $v_o(t) = 4.47 \cos(3t + 26.6^\circ)$ V. Find the average power absorbed by each element in the circuit

4.27 For the circuit shown in Fig. P4.17, when $v_s(t) = 10 \cos 4t V$, then the Thévenin equivalent of the portion of the circuit in the shaded box is $\mathbf{V}_{\infty} =$

